



UNIVERSITI
MALAYSIA
PERLIS

SCHOOL OF ELECTRICAL SYSTEMS ENGINEERING



<https://electrical.unimap.edu.my>



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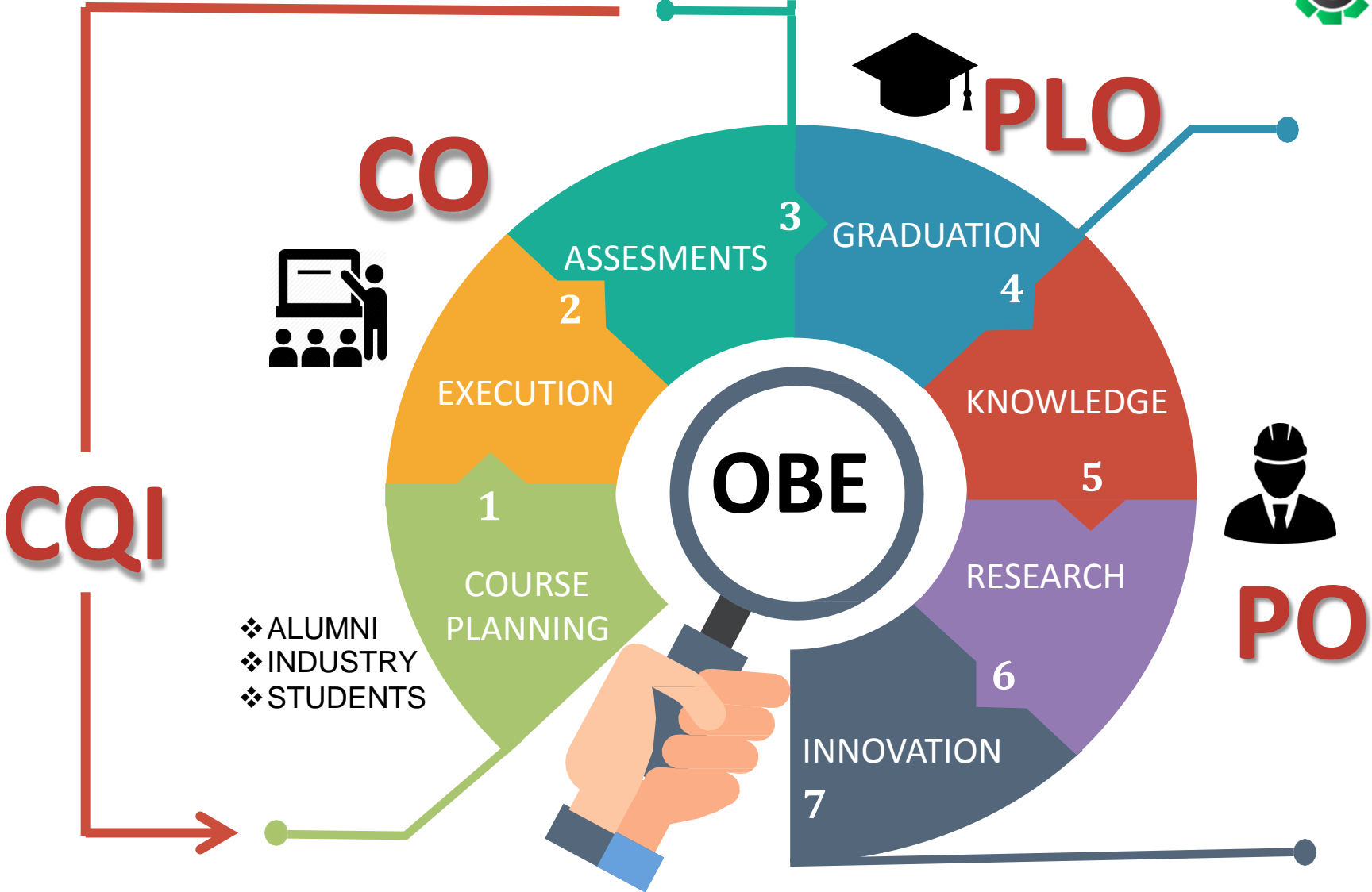
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OUTCOME BASED EDUCATION

Clearly focusing and organizing **education system** around what is essential for all **students** to be **able to do** successfully at the end of their learning experience



OBE IMPLEMENTATION



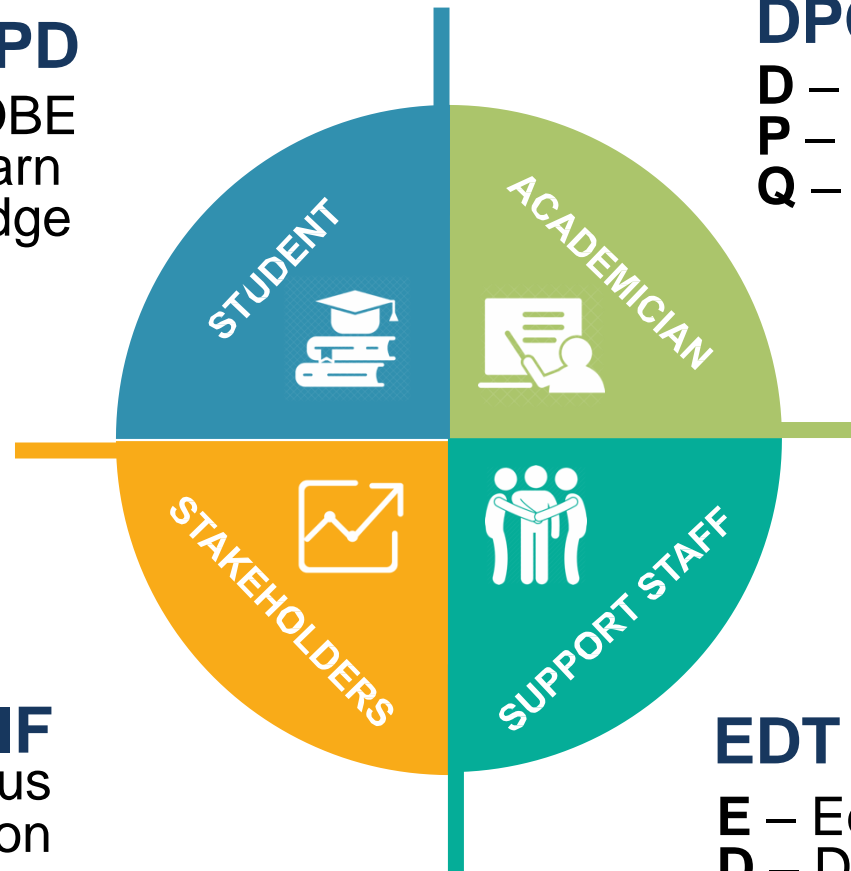
OBE ROLEPLAY

UPD

U – Understand OBE
P – Productively Learn
D – Deliver Knowledge

DPQ

D – Deliver Knowledge
P – Performance Analysis
Q – Quality Improvement



IF

I – Improve Syllabus
F – Feedback on Programmes

EDT

E – Equipment Safety
D – Data/Filling Management
T – Technical Support



KEY CONSTITUENT OF OBE

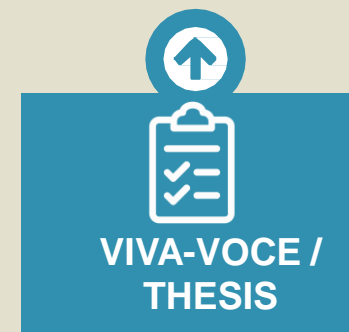
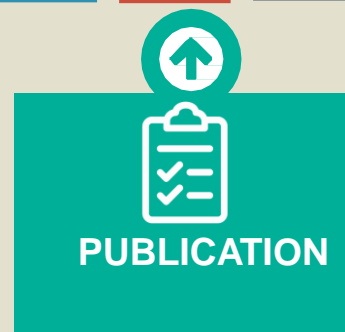
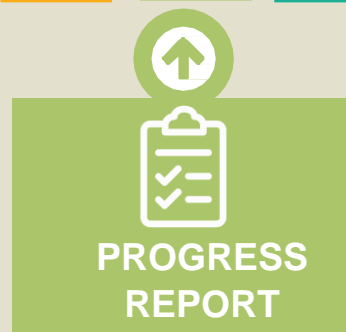


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**UNIVERSITI
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PERLIS**

VISION & MISSION



PO AND PLO

Programme Objectives (PO)



KNOWLEDGE



RESEARCH



INNOVATION

Programme Learning Outcomes (PLO)



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<https://electrical.unimap.edu.my/index.php/academic/obe>

PLO ASSESSMENT



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CGS Guidelines

<https://cgs.unimap.edu.my/index.php/en/form>

click me



- Students must submit their progress report **online** every six (6) months
- System opens for 2 months once open

Thesis Submission Requirement:

- Attend Postgraduate Studies Orientation Programme
- Attend Thesis Writing Workshop
- Pass Research Methodology course
- Pass proposal defence
- Meet publication requirement
- Undergone pre-viva presentation

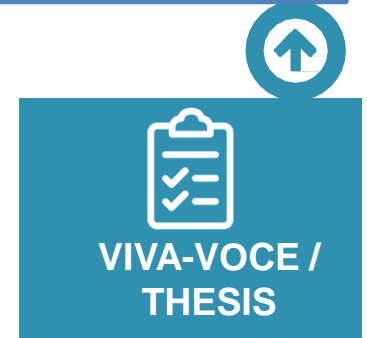


Table 3: Minimum period for proposal defence for PhD and MSc after the main (initial) registration.

Programme	Mode	Minimum Period	Maximum Period
PhD	Full-Time	6 months	12 months
	Part-Time	12 months	24 months
PhD by Publication (PhD. Pub.)	Full-Time	6 months	12 months
	Part-Time	12 months	24 months
MSc	Full-Time	4 months	9 months
	Part-Time	9 months	18 months

Table 1: Scoring system according to types of publication

Types of Publication	Impact Factor	Point
Journal		
ISI	≥ 0.50	15
ISI	< 0.50	10
Scopus	-	8
International	-	4
Others (refereed)	-	2
Proceeding		
Scopus	-	4
International	-	2
Others (refereed)	-	1

Minimum scoring:

- Msc: 5 points
- PhD: 10 points

F.A.Q

1. The process and assessment from registration until graduation.
Proposal defence → Progress report → Publication → Pre-viva → Viva → Correction → Final submission
Attend PG Orientation & Thesis Writing Workshop. Pass Research Methodology course
2. How was the supervision from your supervisor (SV). Close supervision? Independent? no supervision?
3. Can you complaint/evaluate your SV? **Yes, through Progress Report (online) or to top management (Programme Chairman etc.)**
4. PO and PLO disseminations. **SoESE Website (Academic > OBE), Meeting Room, Postgraduate Lecture Room, CERE, Student Room**
5. Facilities.
6. What did you learn in Research Methodology course?
7. Who is the top management, especially Dean? **Dr. Haziah Abdul Hamid**
8. Who is current External Examiner (EE)? **Prof Dr. Hazlie Mokhlis (Universiti Malaya)**
9. Who is current Industrial Advisory Panel (IAP)? **Ir. Dr. Mohd Aizam Talib (TNB R&D)**
10. Free seminar/talk organized by SoESE or UniMAP.
11. If student has a problem, who should he/she meet? **Supervisor or top management (Programme Chairman, TDA, TDH, Dean)**
12. Graduate On Time (GOT). How does student feels about GOT? Is it possible? Does your SV helps you to achieve GOT?
13. Financial aids are available for PG students. **PAAF & PALAF (CGS website), zakat (letter from SoESE)**
14. Supervision Workshop for lecturers by CGS . **Organized periodically**



VISION AND MISSION



Vision

An internationally competitive academic and research institution in electrical engineering



Mission

To produce exemplary individuals who contributes to the electrical engineering



Doctor of Philosophy

Programme Objectives (PO) Doctor of Philosophy (Electrical System Engineering)



PO



PLO

Learning Outcomes (LO) Doctor of Philosophy (Electrical System Engineering)



Master of Science



Programme Objectives (PO) Master of Science (Electrical System Engineering)



PO



PLO



Learning Outcomes (LO) Master of Science (Electrical System Engineering)



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